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## APPLICANTS

Shinobu Kokufu, Osaka, JAPAN;

yes  
DW

Takeshi Suzuki, Osaka, JAPAN;  
 Fumio Echigo, Osaka, JAPAN; Daizo Andoh, Osaka, JAPAN;  
 Tatsuo Ogawa, Atlanta, GA;  
 Yoshihiro Kawakita, Osaka, JAPAN;  
 Satoru Tomekawa, Osaka, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/986,453 11/08/2001 PAT 6,713,688

yes DW

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN P2000-397329 12/27/2000

yes DW

## IF REQUIRED, FOREIGN FILING LICENSE

GRANTED

\*\* 01/21/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	7	4	1
Verified and Acknowledged	<i>[Signature]</i> <i>[Initials]</i>				

## ADDRESS

20277  
 McDERMOTT WILL & EMERY LLP  
 600 13TH STREET, N.W.  
 WASHINGTON , DC  
 20005-3096

## TITLE

Method of Manufacturing Circuit Board.

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
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